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BUR9-1999-0305US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Toshiharu Furukawa et al.

Serial No.: 09/(unknown)

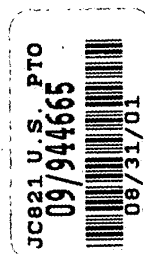
Filed: Concurrently

For: VERTICAL DUAL GATE FIELD EFFECT TRANSISTOR

Assistant Commissioner for Patents
Washington, D.C. 20231

Group Art Unit: Unknown

Examiner: Unknown



INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §§1.97 through 1.99 and pursuant to applicants' duty of disclosure under 37 CFR §1.56, applicants respectfully bring the attached documents, listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application.

The present Information Disclosure Statement is being filed no later than three months from the application's filing date and/or before the mailing date of an Office Action on the merits, whichever is later.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

It is respectfully requested that the documents be considered by the Examiner and formally made of record in this application.

Respectfully submitted,

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